- As announced in the introductory lecture in week 35, we will not follow the course book in the first few lectures.

- The slides that will be used in week 36 can be found via the Blackboard System. We will cover "Single Processor Machines: Memory Hierarchies and Processor Features" with the "Case Study: Tuning Matrix Multiply" in week 36. The slides are the main source of information and should be sufficient for understanding. However, many articles are cited on the slides in case you want to get more information.

- Chapter 3 of the book "Dongarra, J., et al. 2002. The Sourcebook of Parallel Computing, Morgan Kaufmann." has been uploaded in the Blackboard system as additional reading material.

- End of this week the first mandatory homework assignment will be posted.

- Information regarding how to get access to the NERSC supercomputer resources will be posted soon.