- Today we covered pages 44-84 (end of Chapter 2) in the course book. The discussion of cache coherence was focused on false sharing, cache thrashing, and the MESI protocol, which we discussed only briefly (see for example `https://www.cs.tcd.ie/Jeremy.Jones/vivio/caches/MESIHelp`. After introducing how to estimate communication costs in Parallel machines we discussed graph embedding. A very important topic was the usage of bisection width for lower bound estimations. We discussed the performance of fat-meshes and hypercubes with identical costs.

- For the next lecture you should read the course book till end of chapter 3.4.1 (page 130).